

16/12/81



Robert Greene Sterne
Jorge A. Goldstein
David S. Cornwell
Robert W. Esmond
Tracy-Gene G. Durkin
Michael A. Cimbala
Michael B. Ray
Robert E. Sokohl
Eric K. Steffe
Michael Q. Lee
Steven R. Ludwig
John M. Covert
Linda E. Homer
Robert C. Millonig
Donald J. Featherstone
Timothy J. Shea, Jr
Michael V. Messinger
Judith U. Kim

Patrick E. Garrett
Jeffrey T. Helvey
Heidi L. Kraus
Eldora L. Ellison
Thomas C. Fiala
Donald R. Banowitz
Peter A. Jackman
Jeffrey S. Weaver
Brian J. Del Buono
Edward W. Yee
Vincent L. Capuano
Virgil Lee Beaston
Theodore A. Wood
Elizabeth J. Haanes
Joseph S. Ostroff
Frank R. Cottingham
Rae Lynn P. Guest
Daniel A. Klein

Jason D. Eisenberg
Michael D. Specht
Tracy L. Muller
Jon E. Wright
LuAnne M. DeSantis
Ann E. Summerfield
Helene C. Carlson
Cynthia M. Bouchez
Timothy A. Doyle
Gaby L. Longsworth
Lori A. Gordon
Laura A. Vogel
Bryan S. Wade
Bashir M.S. Ali
Shannon A. Carroll
Matthew E. Kelley
Michelle K. Holoubek
Marsha A. Rose

Christopher J. Walsh
W. Blake Coblenz*
James J. Pohl*
Young Tang
John T. Haran*
Mark W. Rygiel
Registered Patent Agents*
Karen R. Markowicz
Matthew J. Dowd
Katrina Yujian Pei Quach
Bryan L. Skelton
Robert A. Schwartzman
Victoria S. Rutherford
Simon J. Elliott
Julie A. Heider
Mita Mukherjee
Scott M. Woodhouse

Liliana Di Nola-Baron
Peter A. Socarras
Jeffrey K. Mills
Danielle L. Letting
Lori Brandes
Of Counsel
Edward J. Kessler
Kenneth C. Bass III
Marvin C. Guthrie

*Admitted only in Maryland
* Admitted only in Virginia
•Practice Limited to
Federal Agencies

December 29, 2006

WRITER'S DIRECT NUMBER:
(202) 772-8817

INTERNET ADDRESS:
BWADE@SKGF.COM

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Art Unit 2181

Attn: Mail Stop Issue Fee

Re: U.S. Utility Patent Application
Application No. 09/395,294; Filed: September 13, 1999
For: **Identification Bit(s) At Predetermined Bit Location(s) To Indicate
Whether An Instruction Defines A Single Operation Or Two
Independent Operations (As Amended)**
Inventor: Sophie WILSON
Our Ref: 1875.5790000

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Amendment Under 37 C.F.R. §1.312; and
2. One (1) return postcard.

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier.

In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

Commissioner for Patents
December 29, 2006
Page 2

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



Bryan S. Wade
Attorney for Applicant
Registration No. 58,228

RES/BSW:smn
Enclosure(s)

624989_1.DOC



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

application of:

Sophie Wilson

Appl. No.: 09/395,294

Filed: September 13, 1999

For: **Identification Bit(s) at
Predetermined Bit Location(s) to
Indicate Whether an Instruction
Defines a Single Operation or Two
Independent Operations**
(as amended)

Confirmation No.: 5796

Art Unit: 2181

Examiner: Meonske, Tonia L.

Atty. Docket: 1875.5790000

Amendment Under 37 C.F.R. § 1.312

Mail Stop Issue Fee

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herein is an Amendment Under 37 C.F.R. § 1.312. As payment of the issue fee has not yet been made or is filed herewith, Applicant respectfully submits that filing under 37 C.F.R. § 1.312 is proper. (M.P.E.P. § 714.16.)

It is believed that extensions of time are not required beyond those that may otherwise be provided for in documents accompanying this Amendment. However, if additional extensions of time are necessary to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required therefor are hereby authorized to be charged to our Deposit Account No. 19-0036.

Amendments to the Title

Please amend the title as follows:

IDENTIFICATION BIT(S) AT [[A]] PREDETERMINED INSTRUCTION BIT
LOCATION(S) THAT TO INDICATE[[S]] WHETHER [[THE]] AN INSTRUCTION IS ONE
DEFINES A SINGLE OPERATION OR TWO INDEPENDENT OPERATIONS AND
INDICATES THE NATURE OF THE OPERATIONS EXECUTING IN TWO PROCESSING
CHANNELS.